

**Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. (Currently Amended) A cell library database composed of basic circuits of multiple types implementing various logics, and stored in a computer readable medium to be used with a computer-aided logic circuit design system in designing an integrated circuit, the database containing on a basic circuit to basic circuit basis:

voltage value information ~~which is given to~~ associated with at least one of a plurality of data terminals composed of input and output terminals included in the basic circuit, the voltage value information including various voltage states to be applied to the at least one of the plurality of data terminals and being ~~and in which different voltage states of one node are~~ represented in multiple bits; and

logical information between the input and output terminals including one or more said data terminals having the voltage value information, in order to allow the design system to conduct verification of an integrated circuit in consideration of voltage variation of a terminal of each basic circuit in the integrated circuit.

2. (Original) The database of claim 1, wherein the data terminal having the voltage value information represented in multiple bits is a power source terminal.

3. (Original) The database of claim 1, wherein the data terminal having the voltage value information represented in multiple bits is a digital input terminal or a digital output

terminal.

4. (Original) The database of claim 1, wherein the data terminal having the voltage value information represented in multiple bits is an analog input terminal or an analog output terminal.

5. (Original) The database of claim 1, wherein the database further contains information on logic delays between the input and output terminals when logic changes occur therebetween.

6. (Original) The database of claim 1, wherein the database further contains withstand voltage information for each of the basic circuits.

7-9. (Cancelled)